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Applicant: Y. AJIOKA
Filing Date:
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U.S. Patent Documents

Examiner Initials	Document No.	Date	Name	Class Subclass	Filing Date If Approp.
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Foreign Patent Documents

Document No.	Date	Country	Class Subclass	Translation Yes No
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15	61-206079	9/86	Japan	
15	5-324954	12/93	Japan	
15	7-175934	7/95	Japan	

Other Documents (including Author, Title, Date, Pertinent Pages, etc.)

15 "Pattern Description with a Highly Parallel Information Processing System I", A. Tojo, Denkishikenjo Ihou, Vol. 31, No. 8, pp. 18-34, 1967

15 "Pattern Description with a Highly Parallel Information Unit (VI) - Construction and Simulation of the System", A. Tojo et al, Denkishikenjo Ihou, Vol. 33, No. 5, pp. 1-27, 1969

15 "Recognition of Hand-Printed Japanese Characters Called HIRAGANA Using Local Parallel Operations, S. Ohyagi, et al, Technical Report of the Institute of Electronics and Communication Engineers, IE76-87, pp. 11-18, 1976

15 "CLIP-4: A Large Scale Integrated Circuit Array Parallel Processor", MJB Duff, Proc. 34d IJCPR, pp.728-733, 1976

15 "MPP: A High-Speed Image Processor, Algorithmically Specialized Parallel Computers", Kenneth E. Batchter, pp. 59-68, 1985

15 "A Data Flow Processor Array System Design and Analysis", N. Takahashi et al, Proc. 10th ISCA, 1983

15 "A Method for Measuring the Center Position and the Total Intensity of an Output Distribution of Matrix Positioned Sensors", M. Ishikawa, Journal of the Society of Instrument and Control Engineers, Vol. 19, No. 5, pp. 23-28, 1983

Other Documents (including Author, Title, Date, Pertinent Pages, etc.)

15' "Two-Dimensional Coordinates Transform Circuit for Parallel Processing Vision", T. Mukai et al, IPSJ Technical Report, Computer Vision, 80-28, pp. 209-214, 1992

18 "Vision Chip Architecture Using General-Purpose Processing Elements for lms Vision System", T. Komuro et al, Proc. 4th IEEE Int. Workshop on Computer Architecture for Machine Perception (CAMP'97), pp. 276-279, 1987

18 "1 ms Target Tracking System Using Massively Parallel Processing Vision", Y. Nakabo et al, Journal of the Robot Society of Japan, Vol. 15, No. 3, pp. 105-109, 1997

18 "Design of Massively Parallel Vision Chip Using General-Purpose Processing Element", T. Komuro et al, Journal of the Institute of Electronics, Information and Communication Engineers, Vol. J81-D-I, No. 2, pp. 70-76, 1998

18 "A Neural Cocktail-Party Processor", C. von Malsburg et al, Biol. Cybern., Vol. 54, pp.29-40, 1986

18 "Sensory Segmentation with Coupled Neural Oscillators", C. von Malsburg et al, Biol. Cybern, Vol. 67, pp.233-242, 1992

18 "Binding by Temporal Structure in Multiple Feature Domains of an Oscillatory Neural Network", T. Schillen et al, Biol. Cybern., Vol. 70, pp. 397-405, 1994

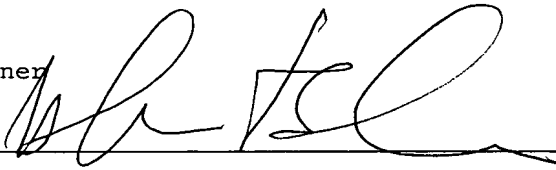
18 "Global Competition and Local Cooperation in a Network of Neural Oscillators", D. Terman et al, Physica, D, 81, pp. 148-176, 1995

18 "Artificial Retinas-Fast, Versatile Image Processing", K. Kyuma et al, Nature, 372, 6502, pp. 197-198, Nov. 1994

18 "Vision Chip's Circuitry has its Eye Out for You", R. Johnson, Techweb News

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